



**RESPONSE UNDER 37 C.F.R. § 1.116  
EXPEDITED PROCEDURE - EXAMINING GROUP 2100**

**PATENT**

I hereby certify that on the date specified below, this correspondence is being deposited with the United States Postal Service as first-class mail in an envelope addressed to Mail Stop AF, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

July 25, 2006  
Date

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No. :	10/686,118	Confirmation No. :	6789
Applicants :	Brian Johnson and Ronnie M. Harrison		
Filed :	October 14, 2003	Attorney Docket No.:	500797.02 (30005/US/2)
Art Unit :	2188	Customer No. :	27,076
Examiner :	Gary J. Portka		
Title :	METHOD AND SYSTEM FOR ADJUSTING THE TIMING OFFSET BETWEEN A CLOCK SIGNAL AND RESPECTIVE DIGITAL SIGNALS TRANSMITTED ALONG WITH THAT CLOCK SIGNAL, AND MEMORY DEVICE AND COMPUTER SYSTEM USING SAME		

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Commissioner for Patents  
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Sir:

Please amend the above-captioned patent application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 16 of this paper.